

Application Data Sheet

Application Information

Application Type:: Regular
Subject Matter:: Utility
Title:: Method For Streamlining Error
Connection Code Computation While
Reading Or Programming A Nand Flash Memory
Attorney Docket Number:: TI-36989 (1962-09800)
Request for Early Publication?:: No
Request for Non-Publication?:: No
Suggested Drawing Figure:: 1
Total Drawing Sheets:: 4
Small Entity?:: No

Applicant Information

Applicant Authority type:: Inventor
Primary Citizenship Country:: France
Status:: Full Capacity
Given Name:: Jean-Yves
Family Name:: SIMON
City of Residence:: St. Laurent du Var
State or Province of Residence:: FRANCE
Country of Residence:: FRANCE
Street of mailing address:: 240 Avenue Paul Cezanne
City of mailing address:: St. Laurent du Var
State or Province of
mailing address:: FRANCE
Country of mailing address:: FRANCE
Postal or Zip Code of
mailing address:: 06700

Correspondence Information

Correspondence Customer Number:: 23494

Representative Information

Representative Customer Number:: 23494

FOREIGN PRIORITY INFORMATION

Country::	Application number::	Filing Date::	Priority Claimed::
EPO	03293051.3	December 4, 2003	Yes

Assignee Information

Assignee name:: Texas Instruments Incorporated
Street of mailing address:: P.O. Box 655474, MS 3999
City of mailing address:: Dallas
State or Province of
mailing address:: TX
Country of mailing
address:: US
Postal or Zip Code of
mailing address:: 75265